

The listing of claims will replace all prior versions, and listings, of claims in the application.

Listing of Claims:

1. (Currently amended) An integrated circuit device, comprising:
a substrate;
an insulating layer disposed on the substrate having a gap formed therein;
a TiN liner layer that exhibits compressive stress characteristics disposed on sidewalls of the insulating layer, which define the gap, and on the substrate in the gap;
a TiN contact plug that exhibits tensile stress characteristics disposed directly on the TiN liner layer; and
a capacitor disposed on an upper surface of the TiN contact plug ~~opposite the substrate~~ and comprising a lower electrode that contacts an upper surface of the TiN contact plug and an upper surface of the TiN liner layer.
2. (Canceled)
3. (Previously presented) The integrated circuit device of Claim 1, wherein the liner layer has an amorphous structure.
4. (Original) The integrated circuit device of Claim 1, further comprising:
an ohmic layer disposed between the liner layer and the sidewalls of the insulating layer, and between the liner layer and the substrate.
5. (Original) The integrated circuit device of Claim 4, wherein the ohmic layer comprise titanium (Ti).
6. (Original) The integrated circuit device of Claim 4, wherein the ohmic layer has a thickness of about 70 Å - 100 Å.
7. (Original) The integrated circuit device of Claim 1, wherein the liner layer has a

thickness of about 200 Å - 500 Å.

8-12. (Canceled)

13. (Currently amended) The integrated circuit device of Claim 1, wherein the lower electrode comprises at least one of the following materials: W, Pt, Ru, Ir, TiN, TaN, WN, RuO₂, and IrO₂.

14. (Previously presented) The integrated circuit device of Claim 1, wherein the gap is wider at a surface of the insulating layer opposite the substrate than the gap is at another location.

15.-24. (Canceled)

25. (Currently amended) A contact plug of a semiconductor device formed through an insulating film interposed between a lower conductive layer and a capacitor to electrically connect the lower conductive layer to the capacitor, comprising:

a TiN plug having an upper surface contacting the upper conductive layer and having tensile stress;

a TiN liner contacting the TiN plug so as to surround the TiN plug along the side wall and the bottom of the TiN plug and having compressive stress; and

an ohmic layer contacting the TiN liner on the opposite side of the TiN plug and located between the TiN liner and the insulating film and between the TiN liner and the lower conductive layer;

wherein the capacitor is disposed on an upper surface of the TiN plug ~~opposite the lower conductive layer and comprising~~ comprises a lower electrode that contacts an upper surface of the TiN contact plug and an upper surface of the TiN liner.

26. (Original) The contact plug of Claim 25, wherein the TiN plug comprises a TiN film formed by chemical vapor deposition (CVD), atomic layer deposition (ALD), metal organic CVD (MOCVD), or metal organic ALD (MOALD).

27. (Original) The contact plug of Claim 25, wherein the TiN liner comprises a TiN film formed by ionized physical vapor deposition (IPVD), metal organic CVD (MOCVD), metal organic ALD (MOALD), sputtering, or collimator sputtering.

28. (Original) The contact plug of Claim 25, wherein the TiN liner has an amorphous structure.

29. (Original) The contact plug of Claim 28, wherein the TiN liner comprises a TiN film formed by ionized physical vapor deposition (IPVD).

30. (Original) The contact plug of Claim 25, wherein the TiN plug has a bottom surface, which contacts the TiN liner, and the upper surface of the TiN plug has a width greater than the width of the bottom surface.

31. (Original) The contact plug of Claim 25, wherein the upper conductive layer comprises at least one film selected from the group of films consisting of W, Al, Pt, Ru, Ir, TiN, TaN, WN, RuO₂, and IrO₂.

32-44. (Canceled)